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09/922742  
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Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

# U.S. UTILITY Patent Application

M.S.	O.I.P.E.	PATENT DATE
SCANNED L M I	Q.A. <i>MT</i>	

APPLICATION NO. 09/922742	CONT/PRIOR F	CLASS 711	SUBCLASS 167	ART UNIT 2185 2186 2187	EXAMINER <i>W. Kim</i>
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APPLICANTS Toshikazu Nakamura

TITLE

Clock synchronized dynamic memory and clock synchronized integrated circuit

PTO-2040  
12/89

## ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>  <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____  <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<b>DRAWINGS</b> Sheets Drwg.    Figs. Drwg.    Print Fig.  _____ (Assistant Examiner)                      (Date)			<b>CLAIMS ALLOWED</b> Total Claims    Print Claim for O.G.  _____	
	_____ (Primary Examiner)                      (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>  _____	
	_____ (Legal Instruments Examiner)                      (Date)			<b>ISSUE FEE</b> Amount Due    Date Paid  _____	
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